

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/810,595
 Confirmation No. 8140
 Filing Date March 15, 2001
 Inventor Belford T. Coursey
 Assignee Micron Technology, Inc.
 Group Art Unit 2813
 Examiner Yennhu B. Huynh
 Attorney's Docket No. MI22-1660
 Customer No. 021567
 Title: Memory Circuitry With Plurality of Capacitors Received Within an
 Insulative Layer Well

RESPONSE TO JULY 15, 2003 NOTICE OF ALLOWANCE
PRELIMINARY AMENDMENT TO ACCOMPANY RCE FILING

To: Mail Stop RCE
 Commissioner for Patents
 P. O. Box 1450
 Alexandria, VA 22313-1450

VIA EXPRESS MAIL

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Responsive to the Notice of Allowance dated July 15, 2003, Applicant remarks as follows:

REMARKS

Claims 26-30, 32-37 and 48-58 remain pending in the application.

There remains prior art which was properly and timely submitted by Applicant but not initialed by the Examiner. Such art consists of an article by

Fuji et al. which was cited along with other art, the other art being initialed, in a Form PTO-1449 which accompanied a Supplemental Information Disclosure Statement dated June 6, 2002 that was filed June 11, 2002. On or about November 7, 2002, the Examiner signed and initialed every other reference on the submitted PTO-1449, but the Fuji et al. reference. A copy of the Form PTO-1449 showing the Examiner's November 7, 2002 initialing of all other art cited thereon is attached, together with copies of the June 6, 2002 Supplemental Information Disclosure Statement and accompanying Form PTO-1449 as originally filed, and including a copy of the Fuji et al. reference cited therein which was not initialed. It is requested that the Examiner consider this prior art reference, and that the Examiner initial it on the Form PTO-1449 and print it on the face of the patent. Such is not seen to be discretionary with the Examiner. See MPEP §§ 609(C)(2) and 609(D).

This application is believed to be in immediate condition for allowance, and action to that end is requested.

Respectfully submitted,

Dated: 10-15-03

By: 
Mark S. Matkin
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